

## INITIAL INFORMATION DATA SHEET

### APPLICATION INFORMATION

Application Type:: Regular  
Subject Matter:: Utility  
Title:: Method and Apparatus for Processing  
Data Within a Programmable Gate  
Array Using Fixed and Programmable  
Processors  
Docket Number:: X-919 US  
Request for Early Pub?:: No  
Request for Non-Pub?:: Yes  
Total Drawing Sheets:: Seven  
Small Entity?:: No

### INVENTOR INFORMATION

Inventor Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Status:: Full Capacity  
Given Name:: Stephen M.  
Family Name:: Douglass  
Street:: 13754 Orleans Court  
City:: Saratoga  
State or Province:: CA  
Postal or Zip Code:: 95070

Inventor Authority Type:: Inventor  
Primary Citizenship Ctry:: Iran  
Status:: Full Capacity  
Given Name:: Ahmad R.  
Family Name:: Ansari  
Street:: 4322 Montmorency Court  
City:: San Jose  
State or Province:: CA  
Postal or Zip Code:: 95118

**CORRESPONDENCE INFORMATION**

Correspondence Customer Number:: 24309

**REPRESENTATIVE INFORMATION**

<b>Representative Customer Number::</b>	<b>24309</b>	
---	--------------	--

**ASSIGNEE INFORMATION**

Assignee Name:: Xilinx, Inc.  
Street:: 2100 Logic Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95124